

ABSTRACT OF THE DISCLOSURE

A data transfer control system receives a command packet ORB transferred through a bus BUS1 (IEEE 1394), issues a command indicated by ORB to a device
5 connected to a bus BUS2 (ATA (IDE) /ATAPI), and orders start of a DMA transfer. The command issued based on ORB is aborted after the completion of the DMA transfer. The data transfer control system compares contents of a command packet ORB1 transferred before a bus reset with contents of a command packet ORB2 transferred after the bus reset. If the contents are different, a command issued based
10 on ORB1 is aborted after completion of a DMA transfer. Dummy data is transferred between the data transfer control system and the device connected to the bus BUS2 until a DMA transfer is completed. Dummy data transfer is controlled by performing a dummy update on a pointer.